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Instruction set being executed by processor performing emulation .... If translated jump to target PC; Else interpret and translate. Tracking SPC ...

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C. Hsien et al, "Java Bytecode to Native Code Translation; The Caffeine Prototype and Preliminary .... 3889243, 4236204, Instruction set modifier register ...

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terpret an memory instruction (need to translate the target. memory address to a valid host address). A high percent. age of hard-to-interpret instructions ...

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6711667, Microprocessor configured to translate instructions from one instruction set to another, and to store the translated instructions, March, 2004 ...

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Instruction set simulation can be speeded up by binary translation [3] .... The thesis describes how to translate Java bytecodes to this internal ...

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We created BOA to be an unexposed architecture with an instruction set specifically designed to support binary translation. ...  
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